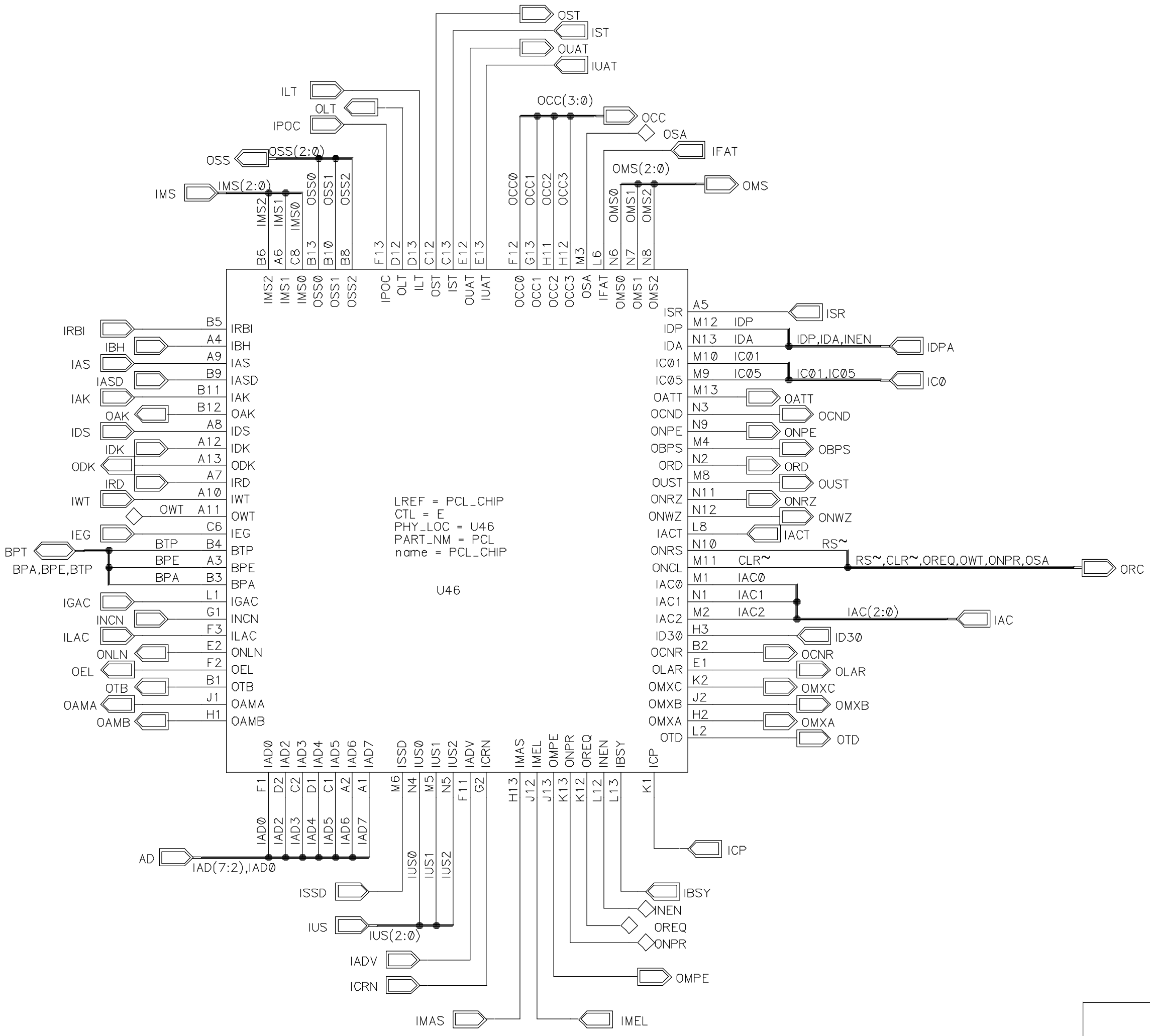

	PAGE 16	TRIUMF	
DESIGNED BY:	A.DAVIEL	787 CCD DATA COMPACTOR MOTHER BOARD	
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CHECKED:		FASTBUS INTERFACE PCL CONNECTIONS	
DATE:	MAY 1991		
DWG LIST:	DL-1485	DWG NO: B-13326	REV 1