RS_9BDF 3
CK1_9BDF 3
CK4_9BDF 3
ENV_9BDF 3

AD9_(7:0) 10

AD9_0 46
AD9_1 47
AD9_2 48
AD9_3 49
AD9_4 50
AD9_5 51
AD9_6 52
AD9_7 53

A_D0 A_D1 A_D2 A_D3 A_D4 A_D5 A_D6 A_D7

26 ENV, 25 CK4, 23 CK1, 22 POC, 59 ID0, 58 ID1, 57 ID2, 56 ID3, 21 SPARE

VCC GND

name = DBD9

A10

PART_NM = DBOARD
CTL = E
LREF = DBOARDN

A0 27 BOA0
A1 28 BOA1
A2 30 BOA2
STB 31
RD 32
BUSY 33
EOB 34
SEL 35

EOB9 3
SEL9 3

DB0 44, DB1 43, DB2 42, DB3 41, DB4 40, DB5 39, DB6 38, DB7 37, DB8 18, DB9 17, DB10 16, DB11 15, DB12 14, DB13 13, DB14 12, DB15 11, DB16 9, DB17 8, DB18 7, DB19 6, DB20 5, DB21 4, DB22 3, DB23 2, DB24 63, DB25 64, DB26 65, DB27 66, DB28 67, DB29 68, DB30 69, DB31 70

BD(31:0)

AD11_(7:0) 10

AD11_0 46
AD11_1 47
AD11_2 48
AD11_3 49
AD11_4 50
AD11_5 51
AD11_6 52
AD11_7 53

A_D0 A_D1 A_D2 A_D3 A_D4 A_D5 A_D6 A_D7

26 ENV, 25 CK4, 23 CK1, 22 POC, 59 ID0, 58 ID1, 57 ID2, 56 ID3, 21 SPARE

VCC GND

'B'

CTL = E
LREF = DBOARDN
name = DBD11

DBOARD

A12

A0 27 BOA0
A1 28 BOA1
A2 30 BOA2
STB 31
RD 32
BUSY 33
EOB 34
SEL 35

EOB11 3
SEL11 3

DB0 44, DB1 43, DB2 42, DB3 41, DB4 40, DB5 39, DB6 38, DB7 37, DB8 18, DB9 17, DB10 16, DB11 15, DB12 14, DB13 13, DB14 12, DB15 11, DB16 9, DB17 8, DB18 7, DB19 6, DB20 5, DB21 4, DB22 3, DB23 2, DB24 63, DB25 64, DB26 65, DB27 66, DB28 67, DB29 68, DB30 69, DB31 70

STB_9BDF 3
RD_9BDF 3

RS_8ACE 3
CK1_8ACE 3
CK4_8ACE 3
ENV_8ACE 3

AD8_(7:0) 10

AD8_0 46
AD8_1 47
AD8_2 48
AD8_3 49
AD8_4 50
AD8_5 51
AD8_6 52
AD8_7 53

A_D0 A_D1 A_D2 A_D3 A_D4 A_D5 A_D6 A_D7

26 ENV, 25 CK4, 23 CK1, 22 POC, 59 ID0, 58 ID1, 57 ID2, 56 ID3, 21 SPARE

VCC GND

CTL = E
LREF = DBOARDN

DBOARD

name = DBD8

A9

A0 27 BOA0
A1 28 BOA1
A2 30 BOA2
STB 31
RD 32
BUSY 33
EOB 34
SEL 35

BUSY 1
EOB8 3
SEL8 3

DB0 44, DB1 43, DB2 42, DB3 41, DB4 40, DB5 39, DB6 38, DB7 37, DB8 18, DB9 17, DB10 16, DB11 15, DB12 14, DB13 13, DB14 12, DB15 11, DB16 9, DB17 8, DB18 7, DB19 6, DB20 5, DB21 4, DB22 3, DB23 2, DB24 63, DB25 64, DB26 65, DB27 66, DB28 67, DB29 68, DB30 69, DB31 70

BD(31:0) 1

AD10_(7:0) 10

AD10_0 46
AD10_1 47
AD10_2 48
AD10_3 49
AD10_4 50
AD10_5 51
AD10_6 52
AD10_7 53

A_D0 A_D1 A_D2 A_D3 A_D4 A_D5 A_D6 A_D7

26 ENV, 25 CK4, 23 CK1, 22 POC, 59 ID0, 58 ID1, 57 ID2, 56 ID3, 21 SPARE

VCC GND

'A'

CTL = E
LREF = DBOARDN
name = DBD10

DBOARD

A11

A0 27 BOA0
A1 28 BOA1
A2 30 BOA2
STB 31
RD 32
BUSY 33
EOB 34
SEL 35

EOB10 3
SEL10 3

DB0 44, DB1 43, DB2 42, DB3 41, DB4 40, DB5 39, DB6 38, DB7 37, DB8 18, DB9 17, DB10 16, DB11 15, DB12 14, DB13 13, DB14 12, DB15 11, DB16 9, DB17 8, DB18 7, DB19 6, DB20 5, DB21 4, DB22 3, DB23 2, DB24 63, DB25 64, DB26 65, DB27 66, DB28 67, DB29 68, DB30 69, DB31 70

RD_8ACE 3

STB_8ACE 3

BOA(2:0) 1

	PAGE 7	TRIUMF	
DESIGNED BY:	A.DAVIEL	0804 787 CCD DATA COMPACTOR MOTHER BOARD CHANNELS 8-11	
DRAWN BY:	A.DAVIEL		
CHECKED:			
DATE:	APR 91		
DWG LIST:	DL-1485	DWG NO: B-13326	REV 1