RS_1357 3
CK1_1357 3
CK4_1357 3
ENV_1357 3

AD1_(7:0) 9
AD1_0 46
AD1_1 47
AD1_2 48
AD1_3 49
AD1_4 50
AD1_5 51
AD1_6 52
AD1_7 53

name = DBD1
CTL = E
LREF = DBOARD1
PART_NM = DBOARD
A2

AD3_(7:0) 9
AD3_0 46
AD3_1 47
AD3_2 48
AD3_3 49
AD3_4 50
AD3_5 51
AD3_6 52
AD3_7 53

CTL = E
LREF = DBOARDN
name = DBD3
DBOARD
A4

EOB1 3
SEL1 3
EOB3 3
SEL3 3

BD(31:0)

STB_1357 3
RD_1357 3
RS_0246 3
CK1_0246 3
CK4_0246 3
ENV_0246 3

AD0_(7:0) 9
AD0_0 46
AD0_1 47
AD0_2 48
AD0_3 49
AD0_4 50
AD0_5 51
AD0_6 52
AD0_7 53

CTL = E
LREF = DBOARD0
DBOARD
A1
name = DBD0

AD2_(7:0) 9
AD2_0 46
AD2_1 47
AD2_2 48
AD2_3 49
AD2_4 50
AD2_5 51
AD2_6 52
AD2_7 53

CTL = E
LREF = DBOARDN
name = DBD2
DBOARD
A3

BUSY 1
EOB0 3
SEL0 3
EOB2 3
SEL2 3

1 BD(31:0)
RD_0246 3
STB_0246 3
1 BOA(2:0)

PAGE 5	TRIUMF
DESIGNED BY: A.DAVIEL	787 CCD DATA COMPACTOR MOTHER BOARD
DRAWN BY: A.DAVIEL	CHANNELS 0-3
CHECKED:	
DATE: APR 91	
DWG LIST: DL-1485	DWG NO: B-13326 REV 1