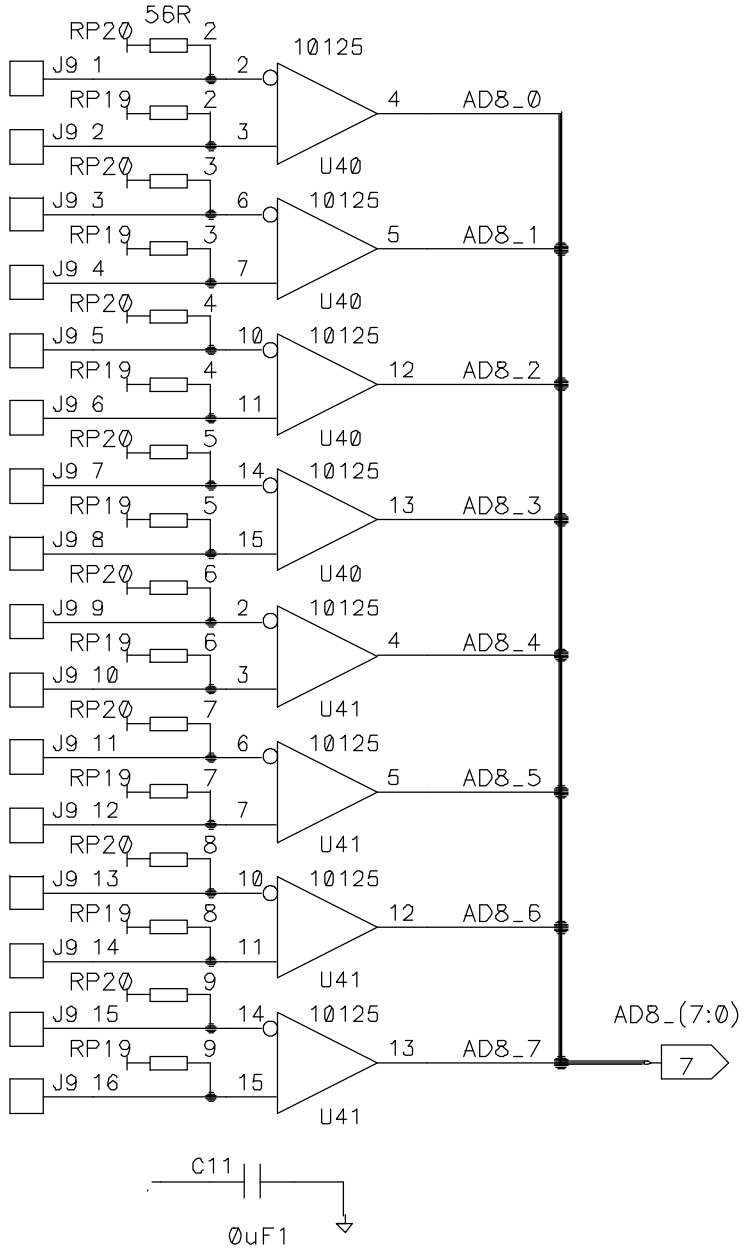
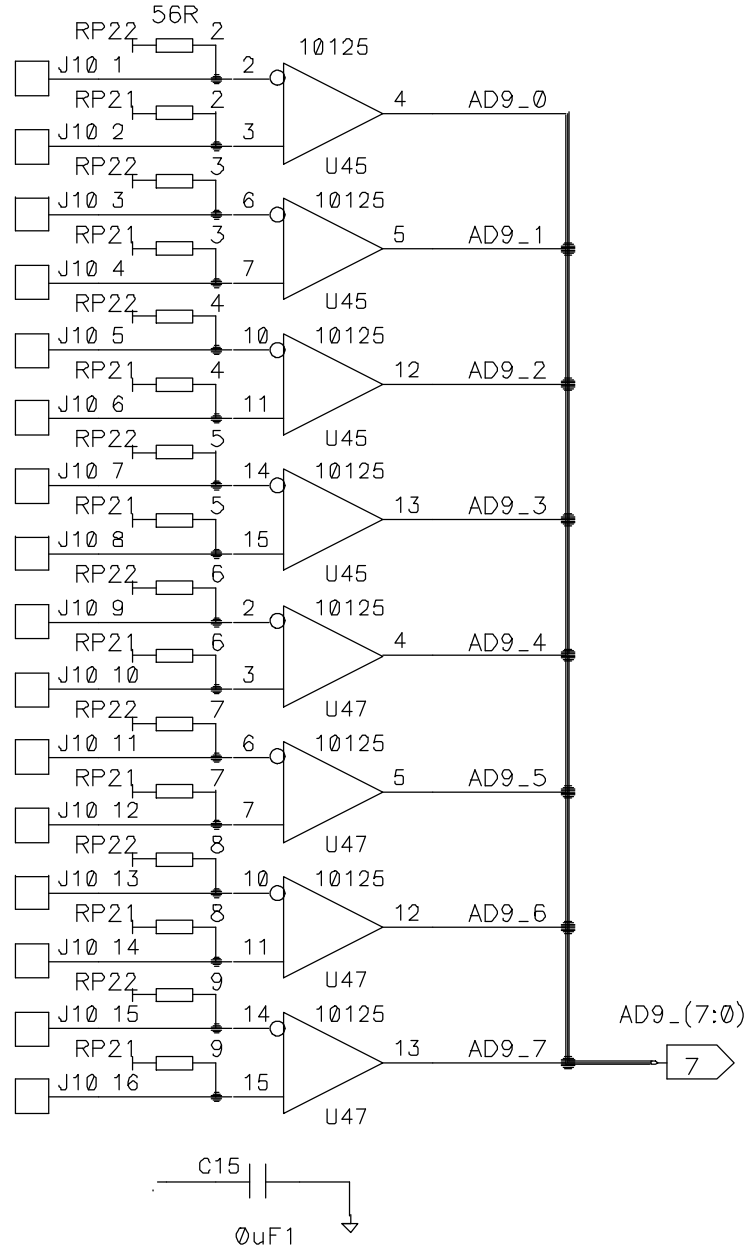
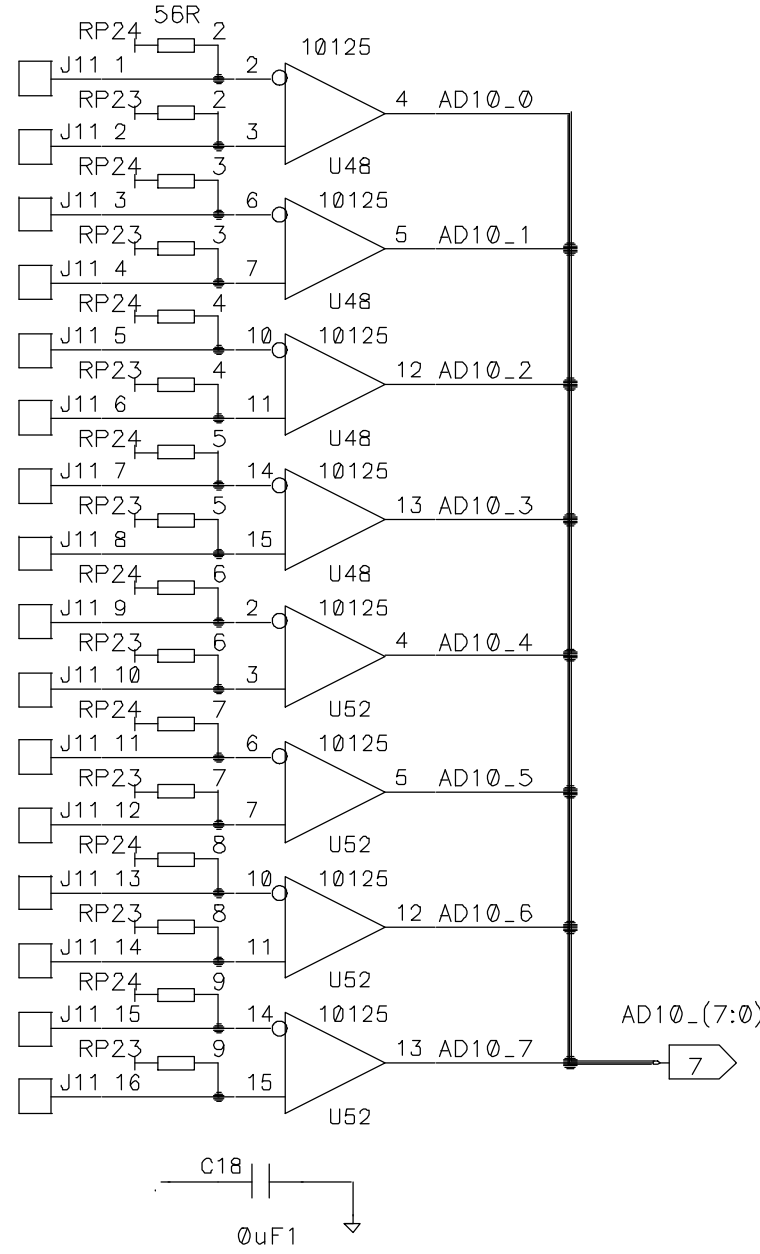
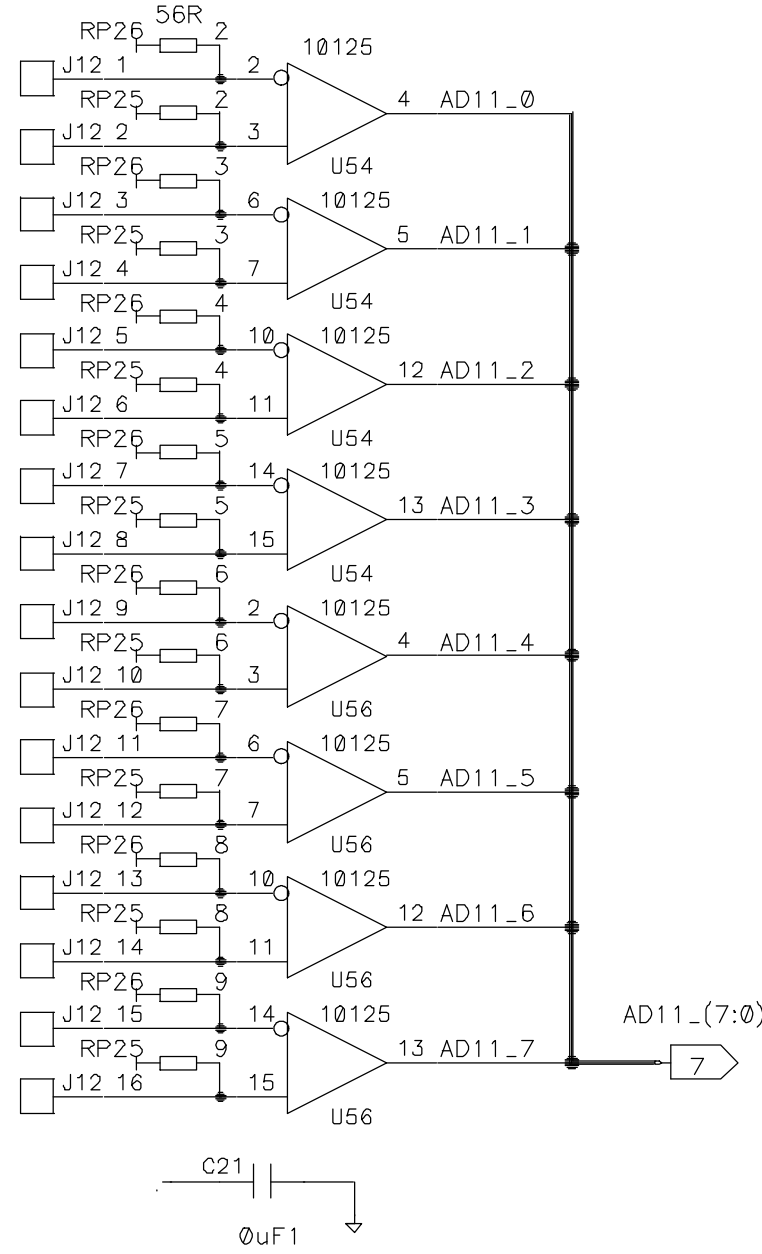
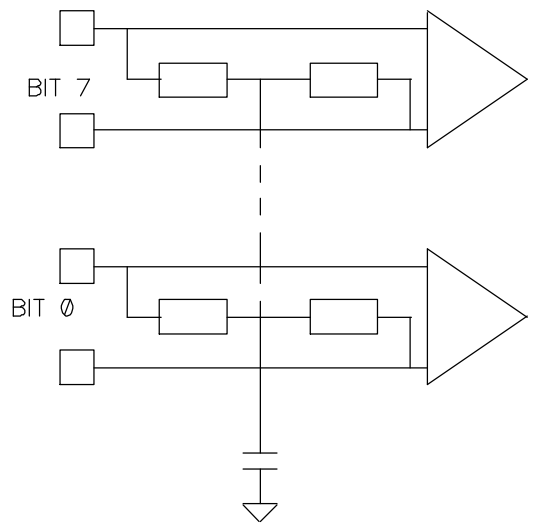








2 RESISTOR PACKS ARE USED WITH PIN 1 COMMON. THIS SIGNAL (VTT) IS LOCAL TO THE CHANNEL AND IS DECOUPLED TO GND, VIZ.:



PAGE 12		TRIUMF
DESIGNED BY:	A.DAVIEL	787 CCD DATA COMPACTOR MOTHER BOARD
DRAWN BY:	A.DAVIEL	
CHECKED:		CHANNELS 8-11 A/D DATA CONNECTORS AND ECL-TTL CONV.
DATE:	MAY 1991	
DWG LIST:	DL-1485	DWG NO: B-13326 REV 1