/USER/PETER/0804/1.DRW  25 MAR 94 15:49  last update: 29 MAY 92 08:09

-1000 -500 0 500 1000

# TRIUMF

CCD DATA COMPACTOR

FASTBUS INTERFACE

| | | | |
|---|---|---|---|
| PAGE 1 | | | |
| DESIGNED BY: | A.DAVIEL | DWG NO: B-13326 | REV 2 |
| DRAWN BY: | A.DAVIEL | | |
| CHECKED: | | | |
| DATE: | MARCH 91 | | |
| DWG LIST: | DL-1485 | | |

TO ENABLE PARTY, JUMPER JP8 2-3
TO DISABLE PARITY, JUMPER JP8 1-2 AND 3-4

ADDRESS DECODER

PULL TRISTATE BUS LOW (FOR 8BIT READ)

SS=0 ON BROADCAST